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## APPLICANTS

Takeshi Sakata, Tokyo, JAPAN;

Tsugio Takahashi, Tokyo, JAPAN;

Katsutaka Kimura, Tokyo, JAPAN; Tomonori Sekiguchi, Tokyo, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 09/349,761 07/09/1999 PAT 6,668,344

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 10-220096 08/04/1998

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 49	TOTAL CLAIMS 23	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature _____ Initials _____				

## ADDRESS

24956  
MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C.  
1800 DIAGONAL ROAD  
SUITE 370  
ALEXANDRIA, VA  
22314

## TITLE

Semiconductor integrated circuit with memory redundancy circuit

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